

Patrick Weber joins Mirror Semiconductor technical advisory board

Friday, 25 May 2007

Patrick Weber, a veteran of three decades in the semiconductor packaging industry and the holder of numerous design and packaging patents, has joined the Technical Advisory Board of Mirror Semiconductor Inc.

Weber is currently president and CTO of Hestia Technologies, Santa Clara, Calif., a designer, developer and manufacturer of advance integrated circuit packages.

Prior to co-founding Hestia in 1983, Weber held top-level engineering positions at both Honeywell Synertek and National Semiconductor Corp., two leading makers of semiconductors. He earned a bachelor's degree in mechanical engineering from the University of Wisconsin, Madison.



"Mirror Semiconductor is gratified to welcome Pat Weber to our Technical Advisory Board. He brings an immense depth of experience and unmatched expertise in many facets of semiconductor packaging to this post," said Martin Hart, president and founder. www.mirrorsemi.com